Express Mailing Label No. El213493168US

Page 1 of 2

Attorney Docket No.: 99-102

**PATENT** 

## **DECLARATION, POWER OF ATTORNEY, AND PETITION**

As a below named inventor, I hereby declare that:

My residence, post office address, and citizenship are as stated below next to my name.

I believe I am the original, first, and sole inventor (if only one name is listed below) or an original, first, and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled "LOW K DIELECTRIC COMPOSITE LAYER FOR INTEGRATED CIRCUIT STRUCTURE WHICH PROVIDES VOID-FREE LOW K DIELECTRIC MATERIAL BETWEEN METAL LINES WHILE MITIGATING VIA POISONING", the specification of which:

	_X_	is attached hereto. was filed on	as Applicati	ion Serial No.	, and wa	s amended on_			
	•	hat I have reviewed and any amendment referre	understand the c				(if applicabl luding th		
Title 37 Thereby	7, Code y claim f or's certi	the duty to disclose info of Federal Regulations, foreign priority benefits u ficate listed below and h date before that of the a	§ 1.56(a). under Title 35, Ur nave also identifie	nited States Coded below any for	e, § 119 of any fol eign application fo	reign applicatio	on(s) for	patent or	
2.	PRIOR FOREIGN APPLICATION(S)						Priority Claimed		
=1							Yes	_ No	
<b>∛N</b> umbe	r)	(Countr	<b>'y</b> )	(Date F	Filed)		Yes	No	
¶umbe	r)	(Countr	y)	(Date F	Filed)		163	_ 110	
insofar in the n materia	as the s nanner p Linforma	the benefit under Title 3 subject matter of each of provided by the first para ation as defined in Title 3 of the prior application a	the claims of thi agraph of Title 39 7, Code of Federa	is application is r 5, United States al Regulations, §	not disclosed in the Code, § 112, I ac 1.56(a), regarding	e prior United S knowledge the events which o	States ap e duty to occurred	oplication disclose	
(Applica	tion Seri	al No.)	(Filing D	Date)	(\$	Status)			
(Applica	tion Seri	al No.)	(Filing D	Date)	(\$	Status)		<u></u>	

I hereby declare that all statements made of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

I hereby appoint John P. Taylor, Reg. 22,369; David G. Pursel, Reg. 28,659; Ralph R. Veseli, Reg. 33,807; Bruce R. Hopenfeld, Reg. 39,714; and Sandeep Jaggi, Reg. 43,331; as my attorneys with full power of substitution and revocation, to prosecute this application and to transact all business in the U.S. Patent and Trademark Office connected therewith and before competent international authorities.



Please send all correspondence to:

Ralph R. Veseli, Patent Attorney Intellectual Property Law Department LSI Logic Corporation 1551 McCarthy Boulevard, M/S D-106 Milpitas, CA 95035 (408) 433-6404

Wherefore I pray that Letters Patent be granted to me for the invention or discovery described and claimed in the foregoing specification and claims, and I hereby subscribe my name to the foregoing specification and claims, declaration, power of attorney, and this petition.

First Inventor's Full Name:	7 7 - 7	G.	Catabay							
Inventor's Signature:	Willy (First)	Marialy	(Last)							
Date: <u>Oct 20</u> ,	<i>1999</i> Country	of Citizenship:	USA							
Residence Address: Saratoga, California Pest Office Address: 13199 McDole Street, Saratoga, CA 95070 USA										
										Second Inventor's Full Name
Date: Oct 21, 1999 Country of Citizenship: USA										
Residence Address:Saratoga, California										
Past Office Address: 12206 Vista Arroyo Court, Saratoga, CA 95070 USA										
्री सिंग्य Inventor's Full Name:										
Inventor's Signature:	(First)	(Initial)	(Last)							
Date: Country of Citizenship:										
Residence Address:										
Post Office Address:	i e									
Fourth Inventor's Full Name:										
Inventor's Signature:	(First)	(Initial)	(Last)							
Date: Country of Citizenship:										
Residence Address:										
Post Office Address:										

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DOCKET NO. 99-102/1D

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

Wilbur G. Catabay and Richard Schinella

Appl. No.

Division of Serial No. 09/426,056

Filed:

Herewith

Title

LOW K DIELECTRIC COMPOSITE LAYER FOR INTEGRATED

CIRCUIT STRUCTURE WHICH PROVIDES VOID-FREE LOW K DIELECTRIC MATERIAL BETWEEN METAL LINES WHILE

MITIGATING VIA POISONING

Grp./ A.U. :

2813

Examiner

Lisa Kilday

Docket No.:

99-102/1D

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## APPOINTMENT OF ASSOCIATE POWER OF ATTORNEY

March 15, 2002

The Honorable Commissioner of Patents and Trademarks
Washington, D.C. 20231

Dear Sir:

I hereby appoint Timothy R. Croll, Reg. 36,771; Leo J. Peters, Reg. 33,562; and Pete R. Scott, Reg. 33,279; Registration No. 29,431; as associate attorneys in this application to

prosecute said application and to transact in connection therewith all business in the United States Patent and Trademark Office.

Respectfully Submitted,

John P. Taylor, No. 22,369 Attorney for Applicants

Telephone No. (909) 699-7551

Mailing Address:

Sandeep Jaggi, Chief Intellectual Property Counsel Intellectual Property Law Department LSI Logic Corporation Mail Stop D-106 1551 McCarthy Blvd. Milpitas, CA 95035